

Attorney Docket No.: 99-102

PATENT

DECLARATION, POWER OF ATTORNEY, AND PETITION

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "LOW K DIELECTRIC COMPOSITE LAYER FOR INTEGRATED CIRCUIT STRUCTURE WHICH PROVIDES VOID-FREE LOW K DIELECTRIC MATERIAL BETWEEN METAL LINES WHILE MITIGATING VIA POISONING", the specification of which:

is attached hereto.
 was filed on _____ as Application Serial No. _____, and was amended on _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. (If applicable)

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLICATION(S)

Priority Claimed

Yes No

(Number)	(Country)	(Date Filed)	Priority Claimed
			Yes <input type="checkbox"/> No <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a), regarding events which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
(Application Serial No.)	(Filing Date)	(Status)

I hereby declare that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

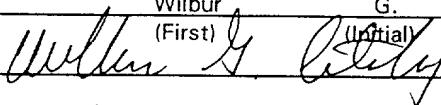
I hereby appoint John P. Taylor, Reg. 22,369; David G. Pursel, Reg. 28,659; Ralph R. Veseli, Reg. 33,807; Bruce R. Hopenfeld, Reg. 39,714; and Sandeep Jaggi, Reg. 43,331; as my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith and before competent international authorities.

Please send all correspondence to:

Ralph R. Veseli, Patent Attorney
Intellectual Property Law Department
LSI Logic Corporation
1551 McCarthy Boulevard, M/S D-106
Milpitas, CA 95035
(408) 433-6404

Wherefore I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the foregoing specification and claims, and I hereby subscribe my name to the foregoing specification and claims, declaration, power of attorney, and this petition.

First Inventor's Full Name: Wilbur G. Catabay
(First) (Initial) (Last)

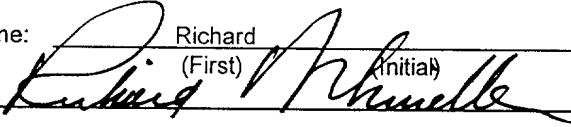
Inventor's Signature: 

Date: Oct 20, 1999 Country of Citizenship: USA

Residence Address: Saratoga, California

Post Office Address: 13199 McDole Street, Saratoga, CA 95070 USA

Second Inventor's Full Name: Richard Schinella
(First) (Initial) (Last)

Inventor's Signature: 

Date: Oct 21, 1999 Country of Citizenship: USA

Residence Address: Saratoga, California

Post Office Address: 12206 Vista Arroyo Court, Saratoga, CA 95070 USA

Third Inventor's Full Name: _____
(First) (Initial) (Last)

Inventor's Signature: _____

Date: _____ Country of Citizenship: _____

Residence Address: _____

Post Office Address: _____

Fourth Inventor's Full Name: _____
(First) (Initial) (Last)

Inventor's Signature: _____

Date: _____ Country of Citizenship: _____

Residence Address: _____

Post Office Address: _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: : Wilbur G. Catabay and Richard Schinella

Appl. No. : Division of Serial No. 09/426,056

Filed: : Herewith

Title : LOW K DIELECTRIC COMPOSITE LAYER FOR INTEGRATED CIRCUIT STRUCTURE WHICH PROVIDES VOID-FREE LOW K DIELECTRIC MATERIAL BETWEEN METAL LINES WHILE MITIGATING VIA POISONING

Grp./ A.U. : 2813

Examiner : Lisa Kilday

Docket No. : 99-102/1D

APPOINTMENT OF ASSOCIATE POWER OF ATTORNEY

March 15, 2002

The Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Dear Sir:

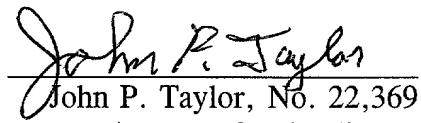
I hereby appoint Timothy R. Croll, Reg. 36,771; Leo J. Peters, Reg. 33,562; and Peter R. Scott, Reg. 33,279; Registration No. 29,431; as associate attorneys in this application to

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prosecute said application and to transact in connection therewith all business in the United States Patent and Trademark Office.

Respectfully Submitted,


John P. Taylor, No. 22,369
Attorney for Applicants
Telephone No. (909) 699-7551

Mailing Address:

Sandeep Jaggi, Chief Intellectual Property Counsel
Intellectual Property Law Department
LSI Logic Corporation
Mail Stop D-106
1551 McCarthy Blvd.
Milpitas, CA 95035